

WEST**Freeform Search****Database:**

US Patents Full-Text Database
 US Pre-Grant Publication Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:

19 and L10

Display: 20 **Documents in Display Format:** CIT, A **Starting with Number** 1**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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Cases

Search History**DATE:** Friday, September 26, 2003 [Printable Copy](#) [Create Case](#)**Set Name Query**

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=NO; OP=ADJ

<u>L11</u>	19 and L10	74	<u>L11</u>
<u>L10</u>	(read or command) near3 queue	3234	<u>L10</u>
<u>L9</u>	l2 near8 (bank or chip or module)	1305	<u>L9</u>
<u>L8</u>	5737757.pn.	1	<u>L8</u>
<u>L7</u>	l2 and L6	2	<u>L7</u>
<u>L6</u>	queue near3 associated near3 (chip or bank or module)	29	<u>L6</u>
<u>L5</u>	l1 and L4	20	<u>L5</u>
<u>L4</u>	(read\$2 or command\$2) with L2	2818	<u>L4</u>
<u>L3</u>	(read\$2 oe command\$2) with L2	0	<u>L3</u>
<u>L2</u>	interleav\$5	38650	<u>L2</u>
<u>L1</u>	queue with associated with (chip or bank or module)	264	<u>L1</u>

<u>L26</u>	(arbiter\$7 or sort\$5 or prioritiz\$5) near5 read\$4	4424	<u>L26</u>
<u>L25</u>	(sort\$5 or prioritiz\$5) near5 read\$4	3961	<u>L25</u>
<u>L24</u>	l22 and L23	104	<u>L24</u>
<u>L23</u>	memory near3 controller	39217	<u>L23</u>
<u>L22</u>	l20 and L21	144	<u>L22</u>
<u>L21</u>	read\$4 near5 (queue or buffer)	32490	<u>L21</u>
<u>L20</u>	l17 and L19	266	<u>L20</u>
<u>L19</u>	(map\$5 or translat\$5 or conver\$5) with address	39588	<u>L19</u>
<u>L18</u>	l6 and L17	5	<u>L18</u>
<u>L17</u>	l13 and l14 and l15 and L16	632	<u>L17</u>
<u>L16</u>	column near3 address	15766	<u>L16</u>
<u>L15</u>	(row or page) near3 address	21752	<u>L15</u>
<u>L14</u>	bank near3 address	3585	<u>L14</u>
<u>L13</u>	chip adj select	7287	<u>L13</u>
<u>L12</u>	l10 and L11	4	<u>L12</u>
<u>L11</u>	(6535955 or 6223301 or 6430702 or 6510100).pn.	4	<u>L11</u>
<u>L10</u>	l6 and l8 and L9	30	<u>L10</u>
<u>L9</u>	memory adj module\$2	9102	<u>L9</u>
<u>L8</u>	dimmm\$2	6281	<u>L8</u>
<u>L7</u>	dimmm	810	<u>L7</u>
<u>L6</u>	raid	3078	<u>L6</u>
<u>L5</u>	l1 and l2 and l3 and L4	0	<u>L5</u>
<u>L4</u>	ecc adj detect	22	<u>L4</u>
<u>L3</u>	host adj controller	1461	<u>L3</u>
<u>L2</u>	data adj controller	6110	<u>L2</u>
<u>L1</u>	bridge	147330	<u>L1</u>

END OF SEARCH HISTORY

<u>L33</u>	(sort\$5 or prioritiz\$5) same l14	28	<u>L33</u>
<u>L32</u>	(sort\$5 or prioritiz\$5) same l13	51	<u>L32</u>
<u>L31</u>	(sort\$5 or prioritiz\$5) same l13 same l14	0	<u>L31</u>
<u>L30</u>	(sort\$5 or prioritiz\$5) same read\$4 same l13	26	<u>L30</u>
<u>L29</u>	(sort\$5 or prioritiz\$5) same read\$4 same l13 same l14	0	<u>L29</u>
<u>L28</u>	(sort\$5 or prioritiz\$5) with read\$4 with l13 with l14	0	<u>L28</u>
<u>L27</u>	l24 and L26	3	<u>L27</u>
<u>L26</u>	(arbiter\$7 or sort\$5 or prioritiz\$5) near5 read\$4	4424	<u>L26</u>
<u>L25</u>	(sort\$5 or prioritiz\$5) near5 read\$4	3961	<u>L25</u>
<u>L24</u>	l22 and L23	104	<u>L24</u>
<u>L23</u>	memory near3 controller	39217	<u>L23</u>
<u>L22</u>	l20 and L21	144	<u>L22</u>
<u>L21</u>	read\$4 near5 (queue or buffer)	32490	<u>L21</u>
<u>L20</u>	l17 and L19	266	<u>L20</u>
<u>L19</u>	(map\$5 or translat\$5 or conver\$5) with address	39588	<u>L19</u>
<u>L18</u>	l6 and L17	5	<u>L18</u>
<u>L17</u>	l13 and l14 and l15 and L16	632	<u>L17</u>
<u>L16</u>	column near3 address	15766	<u>L16</u>
<u>L15</u>	(row or page) near3 address	21752	<u>L15</u>
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<u>L12</u>	l10 and L11	4	<u>L12</u>
<u>L11</u>	(6535955 or 6223301 or 6430702 or 6510100).pn.	4	<u>L11</u>
<u>L10</u>	l6 and l8 and L9	30	<u>L10</u>
<u>L9</u>	memory adj module\$2	9102	<u>L9</u>
<u>L8</u>	dimmm\$2	6281	<u>L8</u>
<u>L7</u>	dimmm	810	<u>L7</u>
<u>L6</u>	raid	3078	<u>L6</u>
<u>L5</u>	l1 and l2 and l3 and L4	0	<u>L5</u>
<u>L4</u>	ecc adj detect	22	<u>L4</u>
<u>L3</u>	host adj controller	1461	<u>L3</u>
<u>L2</u>	data adj controller	6110	<u>L2</u>
<u>L1</u>	bridge	147330	<u>L1</u>

END OF SEARCH HISTORY